

AMENDMENTS TO THE SPECIFICATION

Please replace the paragraph at line 4 on page 2 with the following amended paragraph:

This application is a continuation of United States Serial Number 09/728,469, entitled “Methods and Apparatus for Fast Fourier Transforms,” filed on November 30, 2000, now United States Patent Number 6,609,140, issued on August 19, 2003, which claims priority from United States Patent Application Serial Nos. 60/168,027, filed November 30, 1999, and 60/192,639, filed March 27, 2000, all of which are hereby incorporated by reference.

Please replace the paragraph at line 9 on page 16 with the following amended paragraph:

The FFT method of the invention can be implemented in any manner in which its logic can practically be carried out. The method can be embodied in software that can run on a variety of computers known in the art, including without limitation, personal computers, workstation computers, and supercomputers known in the art. In one preferred embodiment, the FFT method of the invention is implemented in software and executed on a computer having one or more vector processors such as the PowerPC™ G4™ processor having AltiVec™ technology. An example of computer code useful for programming such a computer to carry out the method of the invention is included in United States Patent Application Serial No. 60/168,027, from which this application claims priority, and which is incorporated by reference into this disclosure and is reproduced in the Appendix below. In addition, the logic of the FFT method of the invention can be implemented in a special purpose circuit, such as a circuit including ASICs having the specific logic employed herein deployed within the circuit, or in a system that combines such special purpose circuits with a general purpose computer with software.

Please add the pages attached hereto after page 29 of the application.